A	0 – –
B	1 – –
X	2 – –
ADD	– 0 –
SUB	– 1 –
LOAD	– 2 –
STORE	– 3 –
OR A	3 0 –
NOOP	3 1 –
AND A	3 2 –
LNEG A	3 3 –
IMMED	– – 3
MEMORY	– – 4
INDIRECT	– – 5
INDEXED	– – 6
IND/XED	– – 7
A	0 – –
B	1 – –
X	2 – –
UNC	3 – –
JPD	– 4 –
JPI	– 5 –
JMD	– 6 –
JMI	– 7 –
≠ 0	– – 3
= 0	– – 4
< 0	– – 5
⩾ 0	– – 6
> 0	– – 7
SET 0	0 B 2
SET 1	1 B 2
SKIP 0	2 B 2
SKIP 1	3 B 2
B: BIT POSITION	
RT SFT	0 – 1
RT ROT	1 – 1
LFT SFT	2 – 1
LFT ROT	3 – 1
1 PLC	– 1 –
2 PLC	– 2 –
3 PLC	– 3 –
4 PLC	– 0 –
B REG	+ 4
NOOP	2 0 0
HALT	0 0 0
OF	B0
CA	B1
WS1 A	2 0 1
WS1 B	2 0 2
WS1 X	2 0 3

0 0 0	A
0 0 1	B
0 0 2	X
0 0 3	P
2 0 0	OUTPUT
3 7 7	INPUT

PROGRAM ____________________

VERSION ______ REV ______ DATE ____________

PROGRAMMER ____________________

NOTES:

LOC	DATA	SYMBOLIC ADDRESS	CONTENTS	COMMENTS

A	0 — —
B	1 — —
X	2 — —
ADD	— 0 —
SUB	— 1 —
LOAD	— 2 —
STORE	— 3 —
OR A	3 0 —
NOOP	3 1 —
AND A	3 2 —
LNEG A	3 3 —
IMMED	— — 3
MEMORY	— — 4
INDIRECT	— — 5
INDEXED	— — 6
IND/XED	— — 7
A	0 — —
B	1 — —
X	2 — —
UNC	3 — —
J P D	— 4 —
J P I	— 5 —
JMD	— 6 —
JMI	— 7 —
≠ 0	— — 3
= 0	— — 4
< 0	— — 5
⩾ 0	— — 6
> 0	— — 7
SET 0	0 B 2
SET 1	1 B 2
SKIP 0	2 B 2
SKIP 1	3 B 2
B: BIT POSITION	
RT SFT	0 — 1
RT ROT	1 — 1
LFT SFT	2 — 1
LFT ROT	3 — 1
1 PLC	— 1 —
2 PLC	— 2 —
3 PLC	— 3 —
4 PLC	— 0 —
B REG	+ 4
NOOP	2 0 0
HALT	0 0 0
OF	B0
CA	B1
WS1 A	2 0 1
WS1 B	2 0 2
WS1 X	2 0 3

0 0 0	A
0 0 1	B
0 0 2	X
0 0 3	P
2 0 0	OUTPUT
3 7 7	INPUT

PROGRAM ______________________

VERSION ______ REV ______ DATE ______________

PROGRAMMER ______________________

NOTES:

LOC	DATA	SYMBOLIC ADDRESS	CONTENTS	COMMENTS

A	0 – –
B	1 – –
X	2 – –
ADD	– 0 –
SUB	– 1 –
LOAD	– 2 –
STORE	– 3 –
OR A	3 0 –
NOOP	3 1 –
AND A	3 2 –
LNEG A	3 3 –
IMMED	– – 3
MEMORY	– – 4
INDIRECT	– – 5
INDEXED	– – 6
IND/XED	– – 7
A	0 – –
B	1 – –
X	2 – –
UNC	3 – –
JPD	– 4 –
JPI	– 5 –
JMD	– 6 –
JMI	– 7 –
≠ 0	– – 3
= 0	– – 4
< 0	– – 5
≥ 0	– – 6
> 0	– – 7
SET 0	0 B 2
SET 1	1 B 2
SKIP 0	2 B 2
SKIP 1	3 B 2
B: BIT POSITION	
RT SFT	0 – 1
RT ROT	1 – 1
LFT SFT	2 – 1
LFT ROT	3 – 1
1 PLC	– 1 –
2 PLC	– 2 –
3 PLC	– 3 –
4 PLC	– 0 –
B REG	+ 4
NOOP	2 0 0
HALT	0 0 0
OF	B0
CA	B1
WS1 A	2 0 1
WS1 B	2 0 2
WS1 X	2 0 3

0 0 0	A
0 0 1	B
0 0 2	X
0 0 3	P
2 0 0	OUTPUT
3 7 7	INPUT

PROGRAM ____________

VERSION ______ REV ______ DATE ____________

PROGRAMMER ____________

NOTES:

LOC	DATA	SYMBOLIC ADDRESS	CONTENTS	COMMENTS

A	0 – –
B	1 – –
X	2 – –
ADD	– 0 –
SUB	– 1 –
LOAD	– 2 –
STORE	– 3 –
OR A	3 0 –
NOOP	3 1 –
AND A	3 2 –
LNEG A	3 3 –
IMMED	– – 3
MEMORY	– – 4
INDIRECT	– – 5
INDEXED	– – 6
IND/XED	– – 7

A	0 – –
B	1 – –
X	2 – –
UNC	3 – –
JPD	– 4 –
JPI	– 5 –
JMD	– 6 –
JMI	– 7 –
≠0	– – 3
=0	– – 4
<0	– – 5
≥0	– – 6
>0	– – 7

SET 0	0 B 2
SET 1	1 B 2
SKIP 0	2 B 2
SKIP 1	3 B 2
B: BIT POSITION	

RT SFT	0 – 1
RT ROT	1 – 1
LFT SFT	2 – 1
LFT ROT	3 – 1
1 PLC	– 1 –
2 PLC	– 2 –
3 PLC	– 3 –
4 PLC	– 0 –
B REG	+ 4

NOOP	2 0 0
HALT	0 0 0

OF	B0	
CA	B1	
WS1	A	2 0 1
	B	2 0 2
	X	2 0 3

0 0 0	A
0 0 1	B
0 0 2	X
0 0 3	P
2 0 0	OUTPUT
3 7 7	INPUT

PROGRAM ____________________

VERSION ______ REV ______ DATE ____________

PROGRAMMER ____________________

NOTES:

LOC	DATA	SYMBOLIC ADDRESS	CONTENTS	COMMENTS

A	0 – –
B	1 – –
X	2 – –
ADD	– 0 –
SUB	– 1 –
LOAD	– 2 –
STORE	– 3 –
OR A	3 0 –
NOOP	3 1 –
AND A	3 2 –
LNEG A	3 3 –
IMMED	– – 3
MEMORY	– – 4
INDIRECT	– – 5
INDEXED	– – 6
IND/XED	– – 7
A	0 – –
B	1 – –
X	2 – –
UNC	3 – –
JPD	– 4 –
JPI	– 5 –
JMD	– 6 –
JMI	– 7 –
≠ 0	– – 3
= 0	– – 4
< 0	– – 5
⩾ 0	– – 6
> 0	– – 7
SET 0	0 B 2
SET 1	1 B 2
SKIP 0	2 B 2
SKIP 1	3 B 2
B: BIT POSITION	
RT SFT	0 – 1
RT ROT	1 – 1
LFT SFT	2 – 1
LFT ROT	3 – 1
1 PLC	– 1 –
2 PLC	– 2 –
3 PLC	– 3 –
4 PLC	– 0 –
B REG	+ 4
NOOP	2 0 0
HALT	0 0 0
OF	B0
CA	B1
WS1 A	2 0 1
WS1 B	2 0 2
WS1 X	2 0 3

0 0 0	A
0 0 1	B
0 0 2	X
0 0 3	P
2 0 0	OUTPUT
3 7 7	INPUT

PROGRAM ______________________

VERSION _____ REV _____ DATE ____________

PROGRAMMER ______________________

NOTES:

LOC	DATA	SYMBOLIC ADDRESS	CONTENTS	COMMENTS